

ABSTRACT OF THE DISCLOSURE

The invention concerns a technology for detecting a  
5 frame synchronous pattern comprising:

a data switch section for performing a data  
rearrangement processing of parallel data having a  
given frame synchronous pattern;

10 a temporary region detection section for  
temporarily detecting a candidate of region data  
containing the frame synchronous pattern from the  
parallel data;

15 a frame synchronous pattern detection section  
for detecting the frame synchronous pattern from the  
temporary region data of the temporary region  
detection section; and

20 a data switch control section for controlling  
data rearrangement processing by the data switch  
section according to the detection state of the  
temporary region data by the temporary region  
detection section and to the detection state of the  
frame synchronous pattern by the frame synchronous  
pattern detection section, in order to detect  
precisely the frame synchronous pattern in  $m$  parallel  
25 data without enabling the detection of  $m$  ways of frame  
synchronous patterns in  $m$  parallel data.